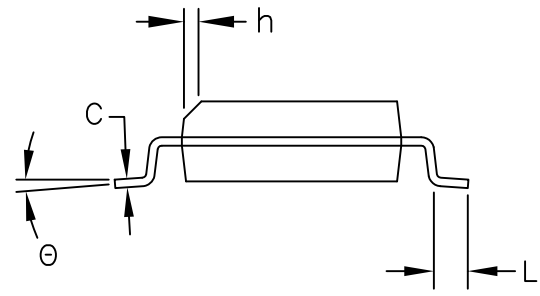


REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	NEW DRAWING	2/95	J.W.
B	UPDATE DIMENSIONS		
C	UPDATE DIMENSIONS	3/99	K.D.



PKG	8 PIN		14 PIN		16 PIN		
LTR	MIN	MAX	MIN	MAX	MIN	MAX	
A	IN. MM	0.053 1.35	0.0688 1.75	0.053 1.35	0.0688 1.75	0.053 1.35	0.0688 1.75
A1	IN. MM	0.004 0.10	0.0098 0.25	0.004 0.10	0.0098 0.25	0.004 0.10	0.0098 0.25
B	IN. MM	0.013 0.33	0.020 0.51	0.013 0.33	0.020 0.51	0.013 0.33	0.020 0.51
C	IN. MM	0.0075 0.19	0.0098 0.25	0.0075 0.19	0.0098 0.25	0.0075 0.19	0.0098 0.25
D	IN. MM	0.189 4.80	0.197 5.00	0.337 8.55	0.344 8.75	0.386 9.80	0.394 10.00
e	IN. MM	.050 BSC 1.27 BSC		.050 BSC 1.27 BSC		.050 BSC 1.27 BSC	
E	IN. MM	0.150 3.80	0.157 4.00	0.150 3.80	0.157 4.00	0.150 3.80	0.157 4.00
H	IN. MM	0.230 5.84	0.244 6.20	0.230 5.84	0.244 6.20	0.230 5.84	0.244 6.20
h	IN. MM	0.010 0.25	0.0196 0.50	0.010 0.25	0.0196 0.50	0.010 0.25	0.0196 0.50
L	IN. MM	0.016 0.40	0.050 1.27	0.016 0.40	0.050 1.27	0.016 0.40	0.050 1.27
theta		0°	8°	0°	8°	0°	8°

THE CHAMFER ON THE BODY IS OPTIONAL. IF IT IS NOT PRESENT, A TERMINAL 1 IDENTIFIER MUST BE POSITIONED SO THAT 1/2 OR MORE OF IT'S AREA IS CONTAINED IN THE HATCHED ZONE.

SIGNATURE		DATE				
DOC. CONTROL:						
ENGR. MGR:						
MFG. ENGR:						
CHECKED BY:						
DRAWN BY: M.W.C.		2/95	TITLE		PACKAGE OUTLINE .150" SOIC 8,14&16 LD.	
			SIZE	FSCM NO	PART NO.	REV
			A		56-G2008-001	C
DO NOT SCALE DWG.			SCALE N/A		SHEET 1 OF 1	