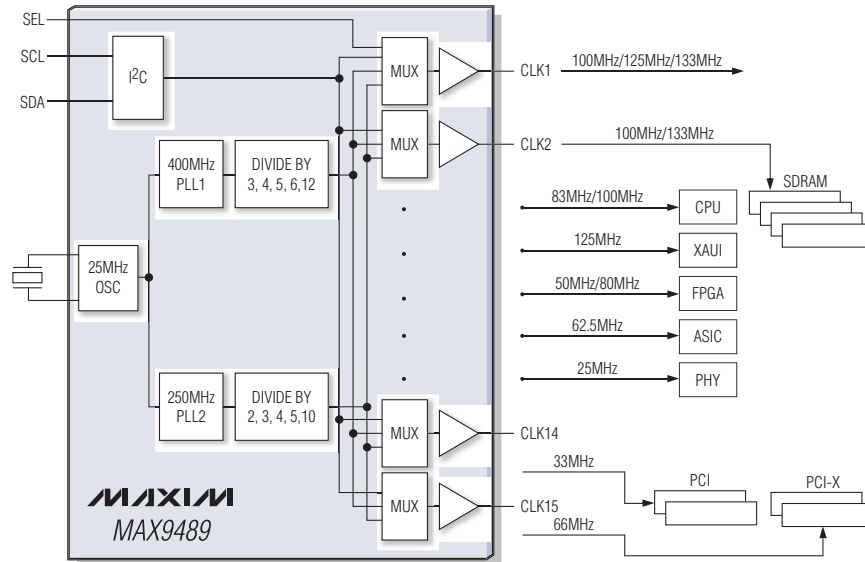


# INTEGRATED HIGH-PERFORMANCE CLOCK GENERATOR SIMPLIFIES YOUR CLOCK TREE

The MAX9489 clock generator with two integrated PLLs provides 15 low-jitter clocks from 25MHz to 133MHz, the commonly used frequencies for datacom, telecom, and storage



- ◆ I<sup>2</sup>C\* Programming Interface
- ◆ ±5% or ±10% Selectable Output-Frequency Margin for Convenient System Testing
- ◆ 15 LVCMOS Outputs with 10 Independent Frequency Selections
- ◆ CLK1 Frequency Set at Power-Up
- ◆ Less than 48ps<sub>RMS</sub> Output Jitter

Part	Input/Crystal Freq (MHz)	No. of Outputs	Outputs Programmable	Output Freq Range (MHz)	Package
MAX9489	25	15	Yes	25 to 133	32-TQFN/TQFP

\*Purchase of I<sup>2</sup>C components from Maxim Integrated Products, Inc., or one of its sublicensed Associated Companies, conveys a license under the Philips I<sup>2</sup>C Patent Rights to use these components in an I<sup>2</sup>C system, provided that the system conforms to the I<sup>2</sup>C Standard Specification defined by Philips.



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