Keywords: clock, loopback, remote framer loopback, payload loopback, local loopback, jitter attenuator, t1, e1, t1/e1, SCT, single chip transceiver, LIU, line interface unit

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APPLICATION NOTE 354

DS2152, DS2154 Clock Map

Abstract: Application Note 354 provides a logical diagram of the clock map of the Dallas Semiconductor/Maxim DS2154 and DS2152 single chip transceivers (SCTs).

Figure 1 logically describes the clock map of the DS2152 and DS2154 Single Chip Transceivers. Although there is only one Jitter Attenuator, which can be placed in the receive or transmit path, two are shown for simplification and clarity.

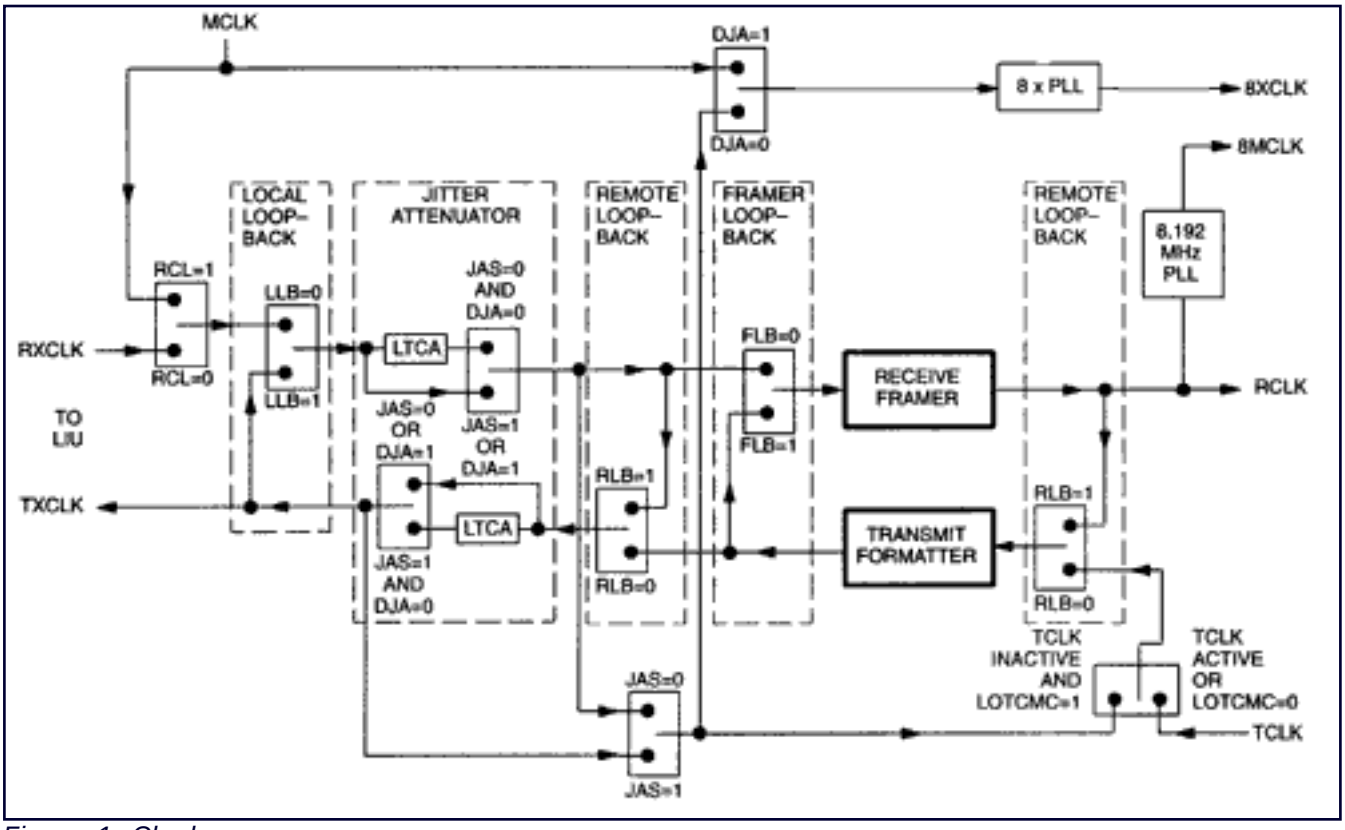

Figure 1. Clock map.

Notes

The PAYLOAD loopback switch is not included on the DS2154

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LTCA = Long Term Clock Average

RLB = Remote LoopBack

FLB = Framer LoopBack

PLB = Payload LoopBack

JAS = Jitter Attenuator Select

DJA = Disable Jitter Attenuator

RCL = Receive Carrier Loss

LLB = Local Loopback

LOTCMC = Loss Of Transmit Clock Mux Control

RXCLK = Recovered clock from LIU

TXCLK = Transmit rate clock for LIU

Application Note 354: http://www.maxim-ic.com/an354

More Information

For technical questions and support: http://www.maxim-ic.com/support

For samples: http://www.maxim-ic.com/samples

Other questions and comments: http://www.maxim-ic.com/contact

Related Parts

DS2152: QuickView -- Full (PDF) Data Sheet -- Free Samples

DS2154: QuickView -- Full (PDF) Data Sheet -- Free Samples

AN354, AN 354, APP354, Appnote354, Appnote 354