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Specifications and Measurement of Local Oscillator Noise in Integrated Circuit Base Station Mixers

Abstract: Residual phase noise from local oscillators (LO) can be degraded by additive noise of on-chip local oscillator drivers/buffers in integrated circuit mixers. Reciprocal mixing of strong RF signals with the LO noise degrades the receiver sensitivity. Specification and evaluation of the LO noise degradation in integrated mixers will enable a system designer to compute the loss of sensitivity in the receiver.

Introduction

Cellular base station receivers work in the presence of high level blockers/interferers while being required to sense weak signals of interest at the antenna. The interferers are usually rejected by filters but only at an intermediate frequency (IF) after the first downconversion. The LNA and mixer preceding the IF filters are required to have high linearity (IP3) and low Noise Figure (NF).

A typical base station receiver featuring simplified blocks is shown in **Figure 1**. The receiver starts with the antenna featuring a tower-top very high-Q cavity-tuned filter, and an LNA located close to the antenna. A long coaxial cable connects the received signal to the transceiver. The transceiver unit has a cascade of LNA, low-noise mixer, SAW filters and IF amplifiers, leading to an analog-to-digital converter (ADC). The first mixer performs a conversion down to a typical IF of 70MHz–100MHz (CDMA 800/GSM 900) or 200MHz–300MHz (GSM 1800/GSM 1900/UMTS).

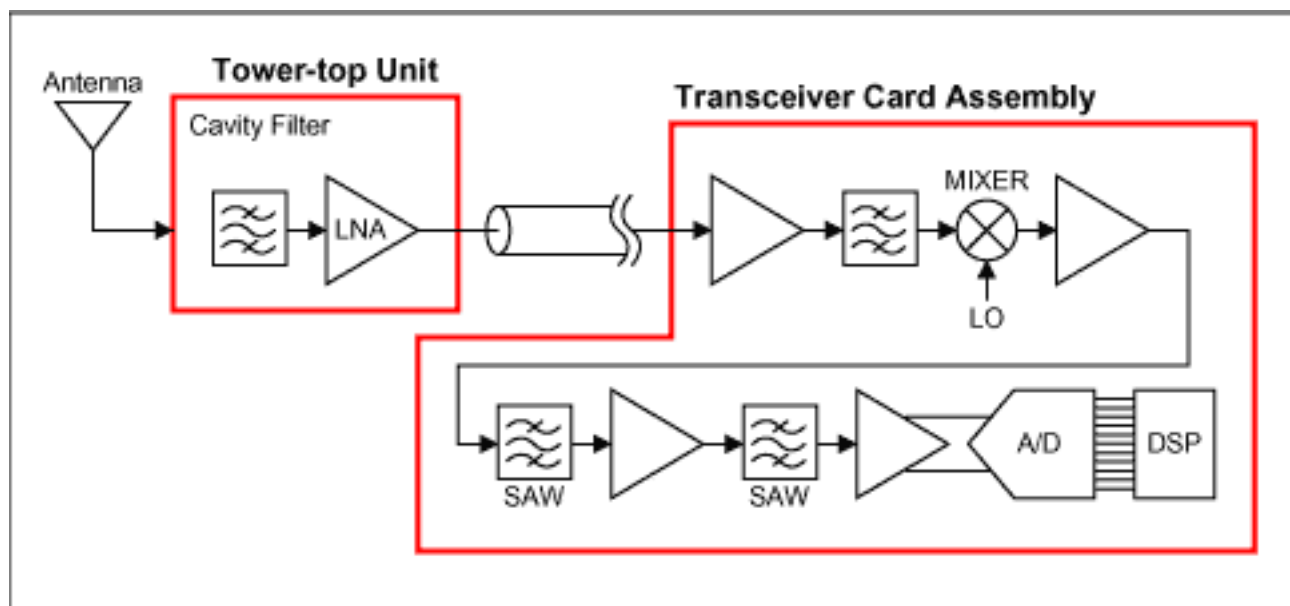


Figure 1. Basic blocks of a cellular base station receiver.

The linearity and noise requirements of the mixer blocks are usually met by passive diode-ring mixers requiring > 17dBm drive into the LO port (**Figure 2**). These base station mixers are discrete designs or hybrid modules driven by external 50Ω buffer amplifiers. Therefore, it is possible to filter the local oscillator residual noise before applying to the mixer. In an IC implementation, care has to be taken to specify and design the local oscillator

driver noise to meet the system requirements. Filtering can be performed at the input to the chip at a lower oscillator level to limit the noise to kT . The LO phase-noise floor is degraded by the buffer amplifier. In the presence of large blocking signals, the receiver noise increases due to reciprocal mixing of the LO noise with the strong interferers.

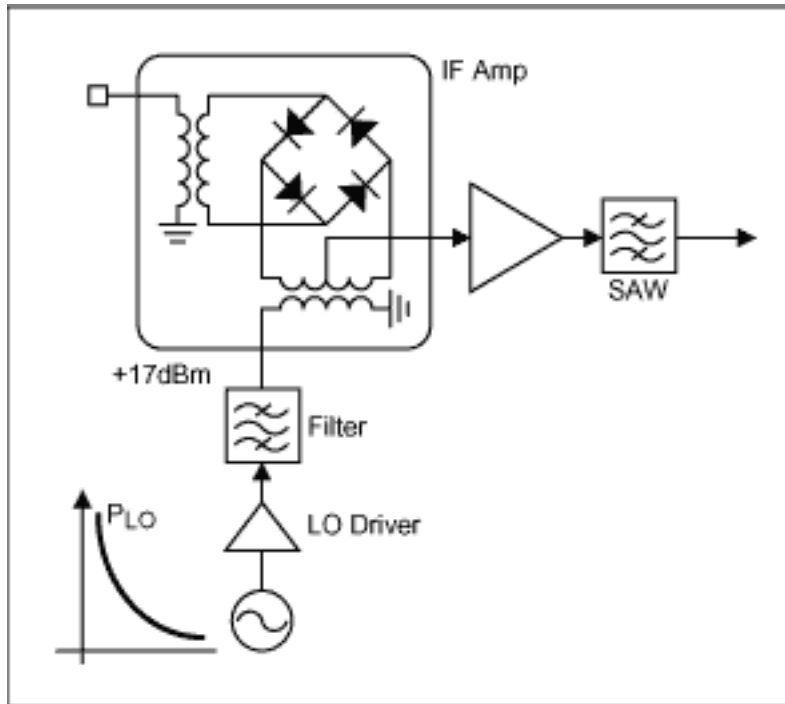


Figure 2. Discrete passive level 17 base station receive mixer with LO filtering.

Mixer Noise Model

Thermal noise is the most commonly specified and measured noise in receive mixers. It describes the noise performance of a mixer that has a 50Ω matched RF input port with a noise power density of -174dBm/Hz (kT_0). The input-referred thermal noise is determined from the noise figure ($10\log_{10}F$) specification of a mixer.

$$N_{\text{th}}^i = kT_0(F - 1) \quad \text{Eq. 01}$$

where

k = Boltzman's Constant (1.381×10^{-23} J/K),

T_0 = Absolute Temperature (290K),

and

F = Noise Factor of the mixer.

Reciprocal mixing happens in the presence of a strong RF signal at the RF port. This is an additional noise not accounted for during NF measurements. Reciprocally mixed noise N_{rm}^i referred to the input can be evaluated at a specific blocker level S_{bl} . Given an LO noise floor \mathcal{L} into the mixer and a bandwidth B , reciprocally mixed noise at IF is

$$N_{\text{rm}}^i = S_{\text{bl}} \mathcal{L} B \quad \text{Eq. 02}$$

The phase noise is assumed flat if the interferer frequency offset is a sufficiently large offset from the desired signal. These two noise sources are independent and can be summed up as illustrated by **Figure 4**. The signal-to-noise ratio degradation from input to output in the presence of blockers can be expressed as

$$\frac{SNR_{in}}{SNR_{out}} = F + \frac{N_{rm}^i}{kT_o B}$$

Eq. 03

Specification of LO Noise

Based on the required carrier-to-interference ratio (C/I) at the output of the mixer, the input blocker level (S_{bl}), the desired signal level (S_{des}) and the receiver noise bandwidth (B), the required LO noise performance in dBc/Hz (\mathcal{L}) can be estimated. For GSM basestations the worst case blocker (S_{bl}) is -13dBm specified for offsets greater than 3MHz. The desired signal sensitivity is -101dBm under blocking conditions. The bandwidth 'B' for GSM system is 200kHz.

$$\mathcal{L} = S_{des} - S_{bl} - \frac{C}{I} - 10 \log_{10} B$$

Eq. 04

The phase-noise floor \mathcal{L} works out to -151dBc/Hz for a GSM basestation requiring a C/I ratio of 10. The actual phase noise should be adB or so better to account for the degradation due to the front-end thermal noise. The mobile station has a less stringent blocker specification leading to $\mathcal{L} = -138$ dBc/Hz.

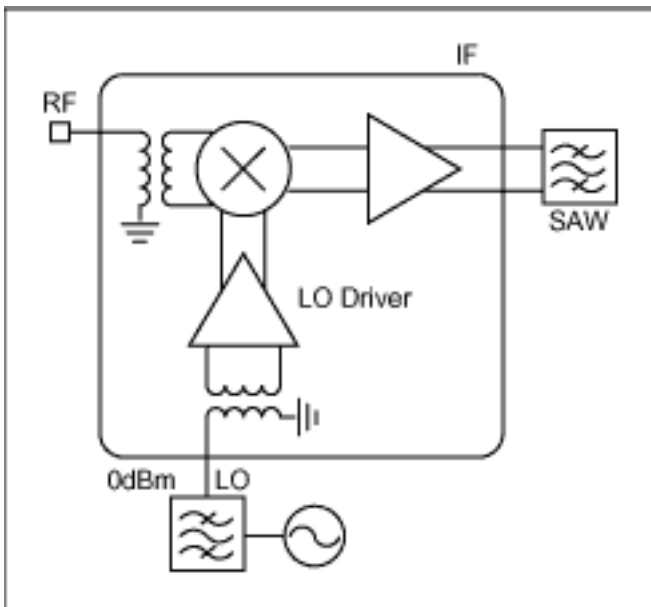


Figure 3. Typical integrated circuit base station receive mixer incorporating LO buffer and gain function at IF.

Effective Noise Floor of LO Driver

Equations 1 and 2 when substituted into 3, describes the effect of the LO noise floor, \mathcal{L} (in dBc/Hz) on the signal-to-noise ratio of the receiver. High linearity mixers typically require high LO drive levels and the noise floor of amplifiers can increase by an order of magnitude. Since reciprocal mixing impresses the LO noise at the IF port as defined by Equation 2, it is possible to measure the noise floor of the LO driver by measurements at IF. A set-up shown in **Figure 5** can be used to measure the mixer noise under blocking conditions. The noise floor is measured under no signal and then under blocking conditions. The N_{bl} is composed of output thermal noise floor, N_{th}^o and output reciprocal mixing noise, N_{rm}^o altered by the SAW filter and IF amplifier. N_{th}^o is determined from small signal noise (F) and gain (G) measurements.

N_{rm}^o can be separated out of N_{bl} and the \mathcal{L} (dBc/Hz) of the integrated LO driver can be computed. Specifying the effective noise floor of integrated circuit mixers helps the system designer estimate the degradation of the Signal-to-Noise ratio (SNR) from Equation 3.

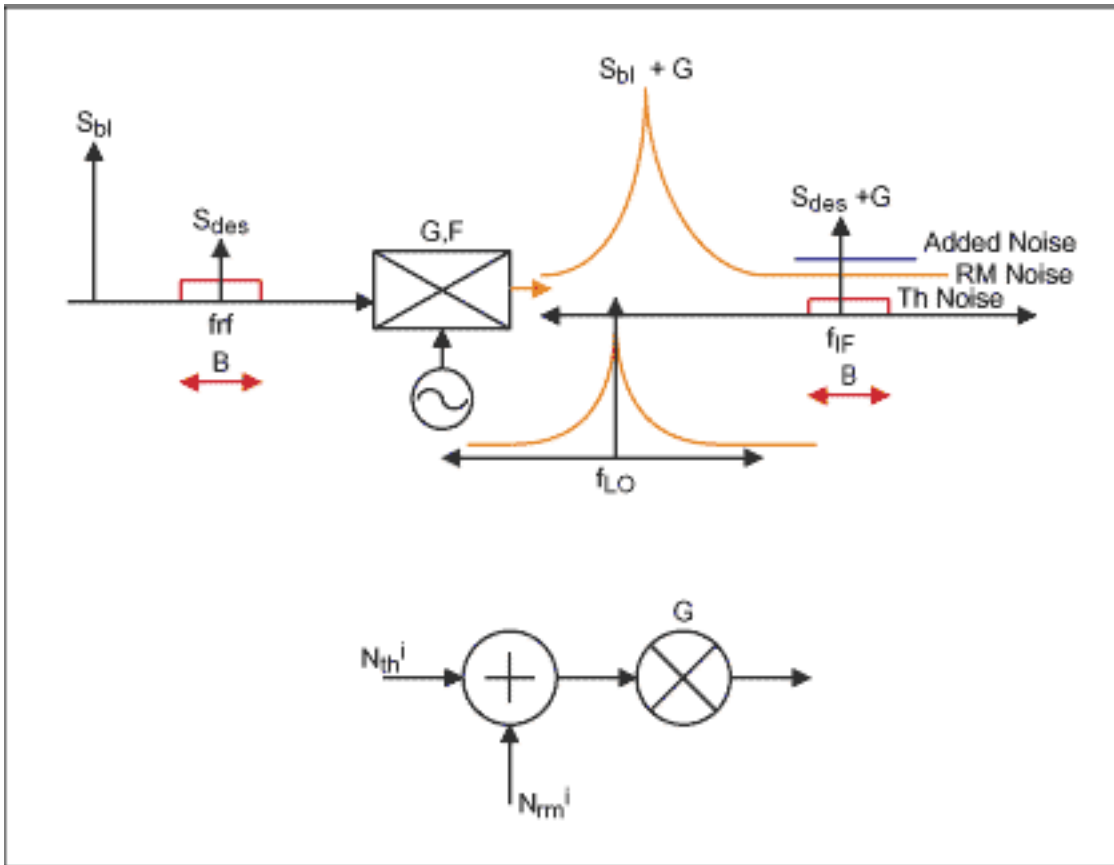


Figure 4. Representation of mixer noise as a sum of thermal and reciprocal mixing noise.

Calculation Example

MAX9993 is an active mixer designed for PCS/DCS/UMTS (1.7G–2.2G) applications. It has a typical gain of 8.5dB, an NF of 9.5dB, an IIP3 = 23.5dBm, a P1dB = 13.0dBm, operating with a local oscillator drive PLO between 0 and 6dBm. The input referred thermal noise, N_{th}^i is $-174 + 9.5 = -164.5$ dBm/Hz and the output thermal noise floor, N_{th}^o is $-174 + 9.5 + 8.5 = -156$ dBm/Hz. The operating frequencies in this example are $f_{IF}=190$ MHz, $f_{LO} = 1800$ MHz and $f_{RF} = 1990$ MHz. Injecting a 5dBm blocker offset 25MHz from f_{RF} ($f_{bl}=2015$ MHz) and using a 190MHz IF Filter (SAWTEK 855770) to reject the blocker at 215MHz, N_{bl} is measured at -127 dBm/Hz with an Agilent E4404B spectrum analyzer. Without the blocker the noise floor of the set-up is measured at $N_{th}^o = -134$ dBm/Hz. The IF amplifier used in the set-up has a gain of 29.5dB and NF of 2.5dB. The measured N_{th}^o agrees with the calculation using the gain and NF of the blocks in the experimental set-up.

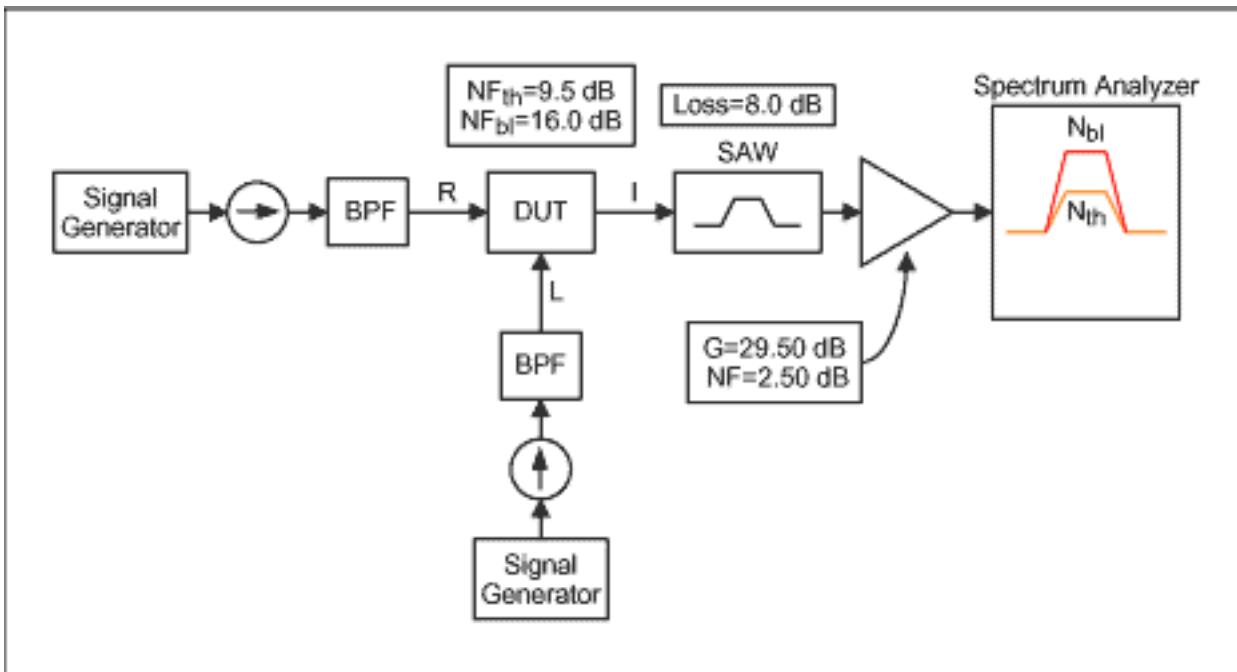


Figure 5. Experimental set-up to measure noise under blocking conditions and deducing \mathcal{L} .

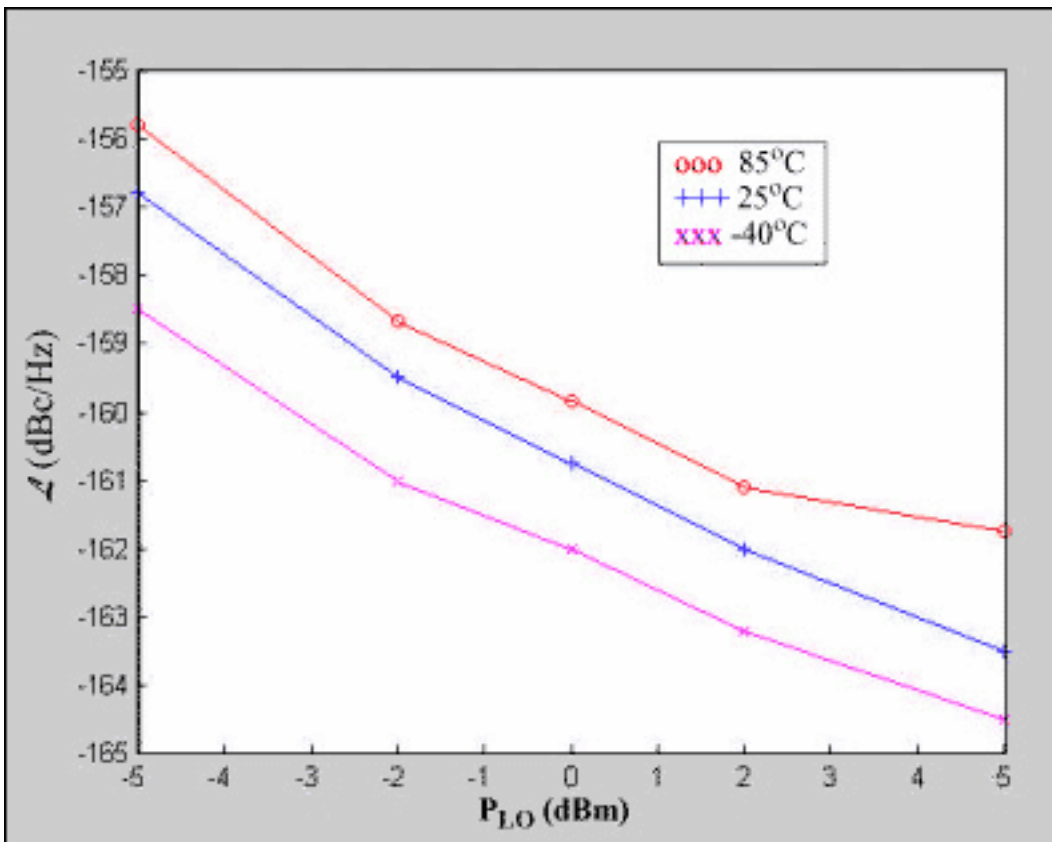


Figure 6. Local oscillator noise (\mathcal{L}) indBc/Hz versus input local oscillator drive power into MAX9982 as a function of temperature.

The increase in noise floor in the presence of a blocker is attributed to the degradation in signal-to-noise ratio output of the mixer from N_{th}^o to N_{bl} . The noise floor (N_{bl}) measured by the spectrum analyzer has noise contributions from thermal, reciprocal mixing noise, SAW and IF amplifier. The mixer's effective NF is increased from 9.5dB to 16dB for cascaded block analysis to account for the measured total output noise power. From the composite noise (N_{bl}), the reciprocally mixed noise N_{rm}^i can be extracted by using the Equations developed in the mixer noise section. The signal-to-noise degradation represented in Equation 3 is 16dB. Solving for N_{rm}^i in Equation 3 results in

$$10\log_{10}\left(\frac{N_{\text{rm}}^i}{kT_o}\right) = 10\log_{10}\left(\left(10^{\frac{16}{10}} - 10^{\frac{9.5}{10}}\right)\right) \cong 15\text{dB} \quad \text{Eq. 05}$$

The input-referred reciprocal mixing noise $N_{\text{rm}}^i = 15\text{dBm/Hz} - 174\text{dBm/Hz} = -159\text{dBm/Hz}$. Since a 5dBm blocker is used, \mathcal{L} is -164dBc/Hz . This is well below the GSM requirement of -151dBc/Hz .

The LO noise defined as a signal-to-noise ratio (dBc/Hz) varies with the LO drive as the drive signal is limited by the LO buffers. This is illustrated in a second example.

The MAX9982 is a cellular CDMA/GSM band (825MHz–915MHz) high-linearity mixer with an input $IP3 > 26\text{dBm}$, a gain of 3dB and an $NF = 11\text{dB}$. This mixer is capable of operating with an LO drive ranging from -5dBm to $+5\text{dBm}$. Most of the specifications are invariant with the drive signal. **Figure 6** shows a plot of \mathcal{L} (dBc/Hz) as a function of the drive-level. The set-up used for this measurement is identical to the one in Figure 5. A 86.6MHz GSM IF filter from SAWTEK (854823) is used to attenuate the blocker signal at the IF (5dBm at the DUT).

Conclusions

The role of the local oscillator buffer amplifier noise in a base station IC mixer design specification has been discussed in this article. Using a simple noise model, the reciprocal mixing component can be extracted from the total noise under blocking conditions. The LO noise of the internal buffer amplifier of two integrated circuit basestation mixers has been characterized by measurement of the total output noise under blocking conditions. This data can be used to compute the loss of receiver sensitivity under blocking conditions.

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