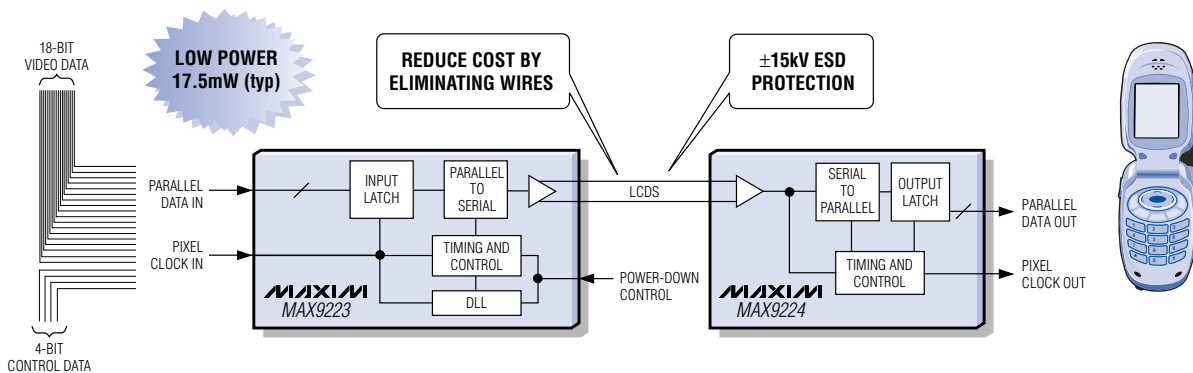


# REDUCE INTERCONNECT WIRING WITH LOW-POWER, *SINGLE-LINK*, 22-BIT SERIALIZER/DESERIALIZER

## Ideal for QVGA and QCIF 320 x 240 Resolution Displays

The MAX9223/MAX9225\* serializers and MAX9224/MAX9226\* deserializers communicate over a single differential link, greatly reducing the amount of interconnect wiring. A serializer/deserializer combination, such as the MAX9223/MAX9224 chipset, serializes the parallel interface of an LCD controller or an image sensor, making the chipset ideal for driving QVGA and QCIF displays.



- ◆ LCD Interface (22-Bit, MAX9223/MAX9224)
- ◆ Camera Interface (10-Bit, MAX9225\* / MAX9226\*)
- ◆ Low-Current Differential Signaling (LCDS) Rejects Common-Mode Noise, Reduces EMI
- ◆ +2.375V to +3.465V Core Supply Voltage
- ◆ Parallel I/O Interfaces Directly with 1.8V to 3.3V Logic
- ◆ ±15kV ESD Protection (Human Body Model)

Part	Description	Parallel Clock (MHz)	Serial Data Rate (Mbps)	Package (mm x mm x mm)
MAX9223	22-bit serializer	5 to 10	110 to 220	28-TQFN (4 x 4 x 0.8)
MAX9224	22-bit deserializer	5 to 10	110 to 220	28-TQFN (4 x 4 x 0.8)
MAX9225*	10-bit serializer	10 to 20	100 to 200	16-TQFN (3 x 3 x 0.8)
MAX9226*	10-bit deserializer	10 to 20	100 to 200	16-TQFN (3 x 3 x 0.8)

\*Future product—contact factory for availability.



[www.maxim-ic.com](http://www.maxim-ic.com)

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