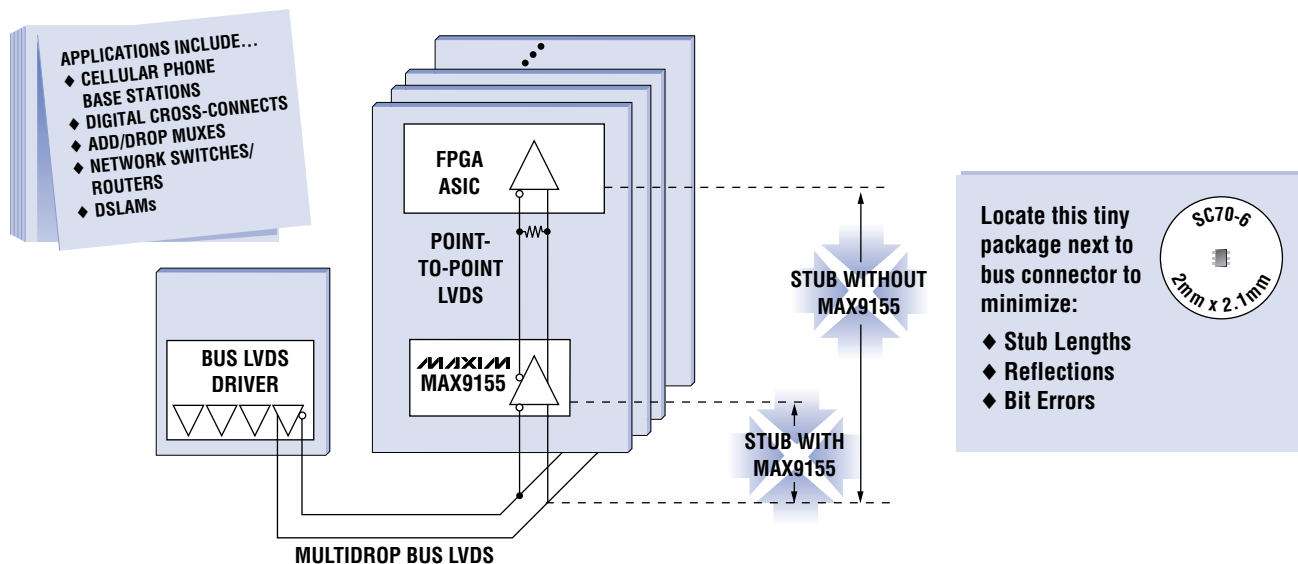


SMALLEST LVDS REPEATER MINIMIZES STUBS IN MULTIDROP BUSES

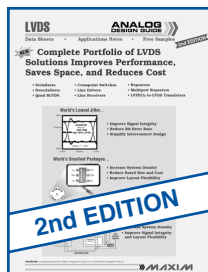
Minimum 0.5ns Output Rise/Fall Time Further Reduces Noise and Reflections



- ◆ High-Impedance Inputs in Power-Down Mode
- ◆ 0.5ns (min) Output Rise/Fall Time Minimizes Radiated Noise
- ◆ Ultra-Low Jitter:
 - ◆ 23ps_{P-P} Added Deterministic Jitter (2²³ - 1 PRBS)
 - ◆ 0.6ps_{RMS} Added Random Jitter
- ◆ 200Mbps Guaranteed Data Rate
- ◆ Fail-Safe Circuit Sets Output High for Undriven Inputs: Open, Terminated, or Shorted (MAX9155)
- ◆ Low 10mA Supply Current
- ◆ Low 6mA Supply Current in Fail-Safe
- ◆ 0.6ns (max) Chip-to-Chip Skew

PART	INPUT	OUTPUT	GUARANTEED DATA RATE (Mbps)	MIN OUTPUT RISE/FALL TIME (ns)	ADDED RANDOM JITTER (pSRMS)	ADDED DETERMINISTIC JITTER (pSP-P)	PIN-PACKAGE	PRICE† (\$)
MAX9155EXT-T	LVDS	LVDS	200	0.5	0.6	23	6-SC70	0.95
MAX9156EXT-T	LVPECL	LVDS	200	0.5	0.6	23	6-SC70	0.95

†2.5k piece price provided is for design guidance and is FOB USA. International prices will differ due to local duties, taxes, and exchange rates. Not all packages are offered in 1k increments, and some may require minimum order quantities.



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