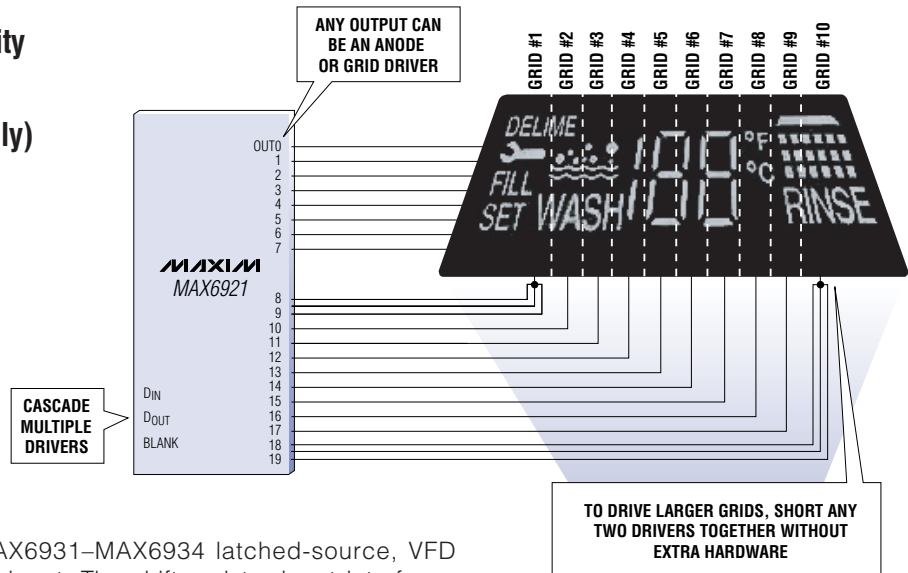


VFD DRIVERS SOURCE 40mA CONTINUOUSLY WITH SLEW-RATE LIMITING TO REDUCE EMI

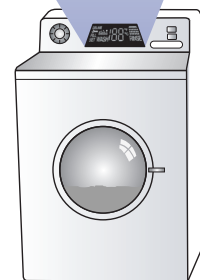
Robust Push-Pull CMOS Outputs Can Be Paralleled for Higher Current Drive

- ◆ 76V Grid/Anode Supply Capability
- ◆ -11V to 0V V_{SS} —Filament Bias Supply (MAX6931–MAX6934 only)
- ◆ PWM Intensity Control with BLANK Input
- ◆ Small TSSOP/QFN Packages
- ◆ -40°C to +125°C Temp Range



The MAX6920/MAX6921/MAX6922/MAX6931–MAX6934 latched-source, VFD drivers feature a 12-bit to 32-bit serial input. The shift-register input interfaces directly to a microprocessor at data rates up to 5MHz. A D_{OUT} device option allows multiple drivers to be cascaded to drive large displays. The slew-rate-limited driver outputs are 76V-tolerant push/pull CMOS transistors with 40mA/4mA source/sink current. A separate V_{SS} supply input can be used to bias the filament supply, simplifying the design (MAX6931–MAX6934). By activating the BLANK input, individual grids or all grids can be blanked to shut down the display. A PWM pulse on the BLANK input provides brightness control over the display.

Part	No. Bits	V_{SS} SUPPLY	D_{OUT} SUPPLY	Pin-Package
MAX6920	12	-	Yes	20-SO
MAX6921	20	-	Yes	28-PLCC, 28-SO, 28-TSSOP
MAX6922	32	-	Yes	44-PLCC
MAX6931	20	Yes	-	28-TSSOP
MAX6932	27	Yes	Yes	36-SSOP
MAX6933	28	Yes	-	36-SSOP
MAX6934	32	Yes	Yes	44-QFN, 44-PLCC



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