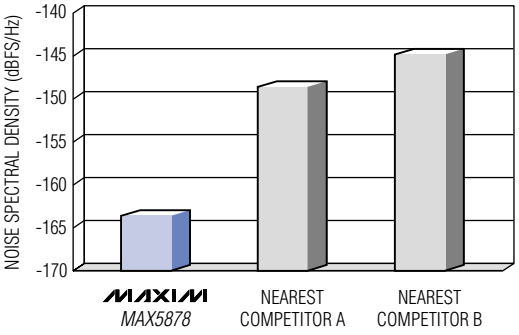
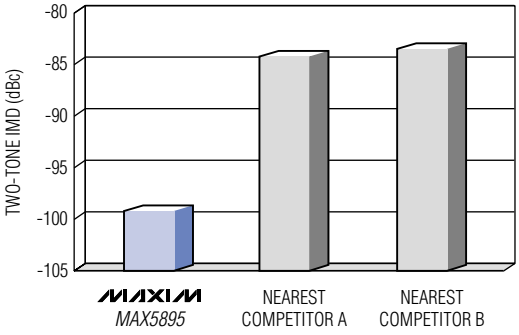
16-BIT, 250Msps DUAL DAC DELIVERS -164dBFS/Hz NOISE DENSITY AT 296mW

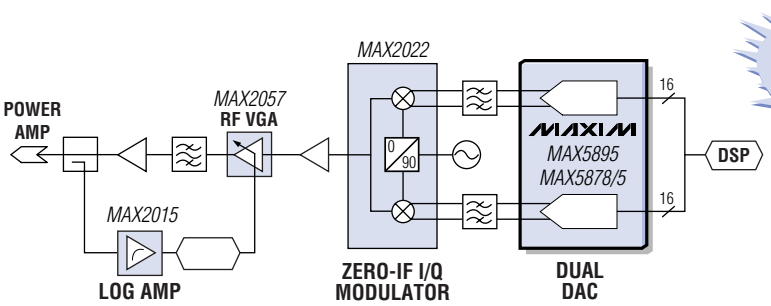
High-Speed 12-/14-/16-Bit, 200/250/500Msps, Dual DACs Are Ideal for Broadband Communications

SUPERIOR NOISE PERFORMANCE



SUPERIOR TWO-TONE INTERMODULATION





Ideal Applications
* Multicarrier Base Stations
* Broadband Wireless Transmitters
* Cable Infrastructure

MAX5878 16-Bit, 250Msps Dual DAC

- Noise Density = -164dBFS/Hz at f_{OUT} = 16MHz
- ACLR = 75dB at f_{OUT} = 61.44MHz
- Low-Power Operation: 296mW at 250Msps
- LVDS and CMOS Interface Options
- Pin-Compatible 12-/14-/16-Bit Families

MAX5895 16-Bit, 500Msps Dual DAC

- Two-Tone IMD = -100dBc at f_{OUT} = 10MHz
- SFDR = 92dBc at f_{OUT} = 10MHz
- Selectable 2x/4x/8x Interpolating Filter with >99dB Stopband Rejection
- Digital Quadrature Modulator with Image Rejection

Part	Resolution (Bits)	DAC Update Rate (Msps)	Interpolating Filter	Modulator	Input Interface
MAX5895/MAX5894/MAX5893	16/14/12	500	Yes	Yes	CMOS
MAX5878/MAX5877/MAX5876	16/14/12	250	—	—	LVDS
MAX5875/MAX5874/MAX5873	16/14/12	200	—	—	CMOS

MAXIM

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