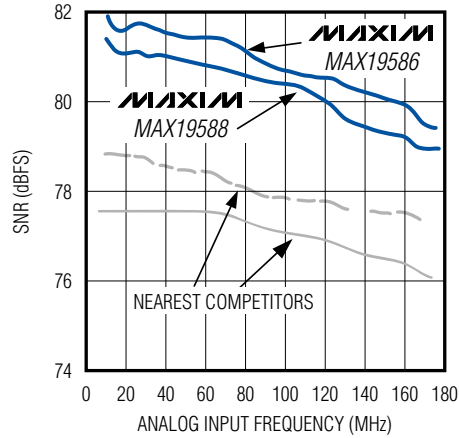
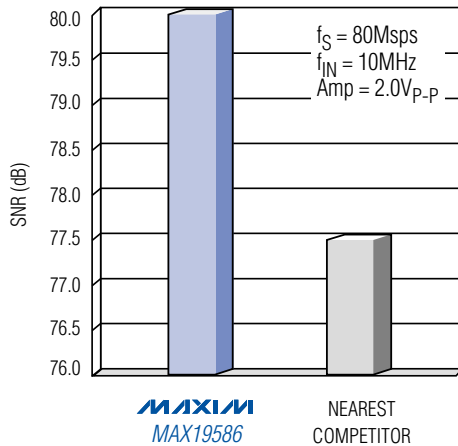


16-BIT, 80Msps/100Msps ADCs HAVE THE BEST NOISE FLOOR AT -82dBFS

Industry-Leading Dynamic Performance at Low Power

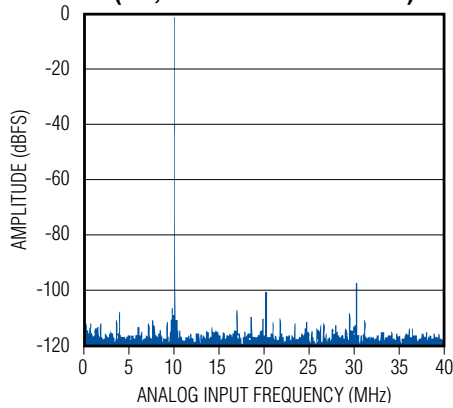
BEST WIDEBAND SNR PERFORMANCE



◆ Outstanding Dynamic Performance

- ◆ -82dBFS Noise Floor
- ◆ 80dB SNR at $f_{IN} = 10\text{MHz}^*$
- ◆ 78.5dB SNR at $f_{IN} = 100\text{MHz}^*$
- ◆ 96dBc SFDR at $f_{IN} = 10\text{MHz}^*$
- ◆ 84dBc SFDR at $f_{IN} = 100\text{MHz}^*$
- ◆ Low Power: 1.1W*
- ◆ Ideal for Multicarrier Receivers, Multistandard Receivers, and High-Performance Instrumentation

FFT PLOT (32,768-POINT RECORD)



*MAX19586, $f_s = 80\text{Msps}$



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