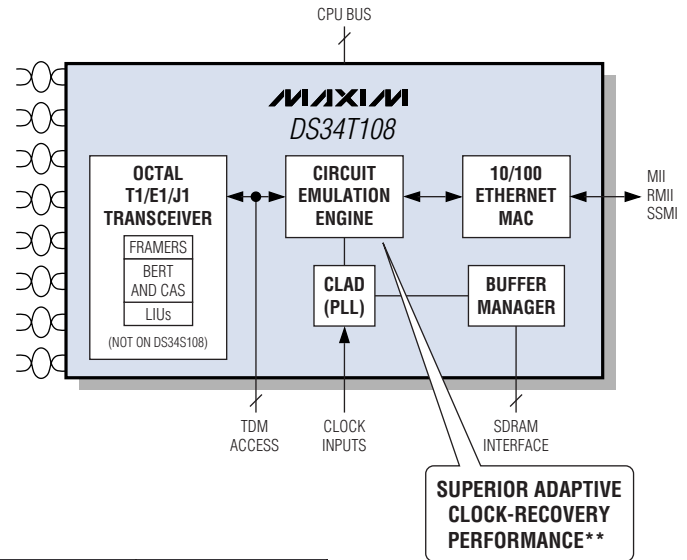


FIRST TDM-OVER-PACKET CONVERTERS TO INTEGRATE FRAMERS AND LIUs

Features that Beat the Competition

- ◆ Eliminate High-Cost Processors
 - ◆ On-Chip Hardware Resources Manage TDM Traffic and Perform Clock Recovery
 - ◆ Host Processor Not in Data Path
- ◆ Independent Clock-Recovery Engines Provide Recovered Clock and Frame Sync Outputs for Each TDM Port
- ◆ More Pseudowire Modes: CESoPSN, SAToP, HDLC, TDMoIP
- ◆ Clock-Recovery Modes: Adaptive, Common Clock (Differential with RTP), External Clock, Loopback

INTEGRATED FRAMERS AND LIUs REDUCE COST AND BOARD SPACE BY OVER 30%



Part	No. of Ethernet Ports	T1/E1/J1 or Serial TDM	Temperature Range (°C)	Package (mm x mm)
DS34T108GN	1	8 T1/E1/J1	-40 to +85	484-HSBGA (23 x 23)
DS34T104GN*		4 T1/E1/J1		484-TEBGA (23 x 23)
DS34T102GN*		2 T1/E1/J1		
DS34T101GN*		1 T1/E1/J1		
DS34S108GN*		8 TDM		256-CSBGA (17 x 17)
DS34S104GN*		4 TDM		
DS34S102GN*		2 TDM		
DS34S101GN*		1 TDM		

*Future product—contact factory for availability.

**A clock-recovery performance report can be requested at: www.maxim-ic.com/TDMoP.



www.maxim-ic.com/TDMoP

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