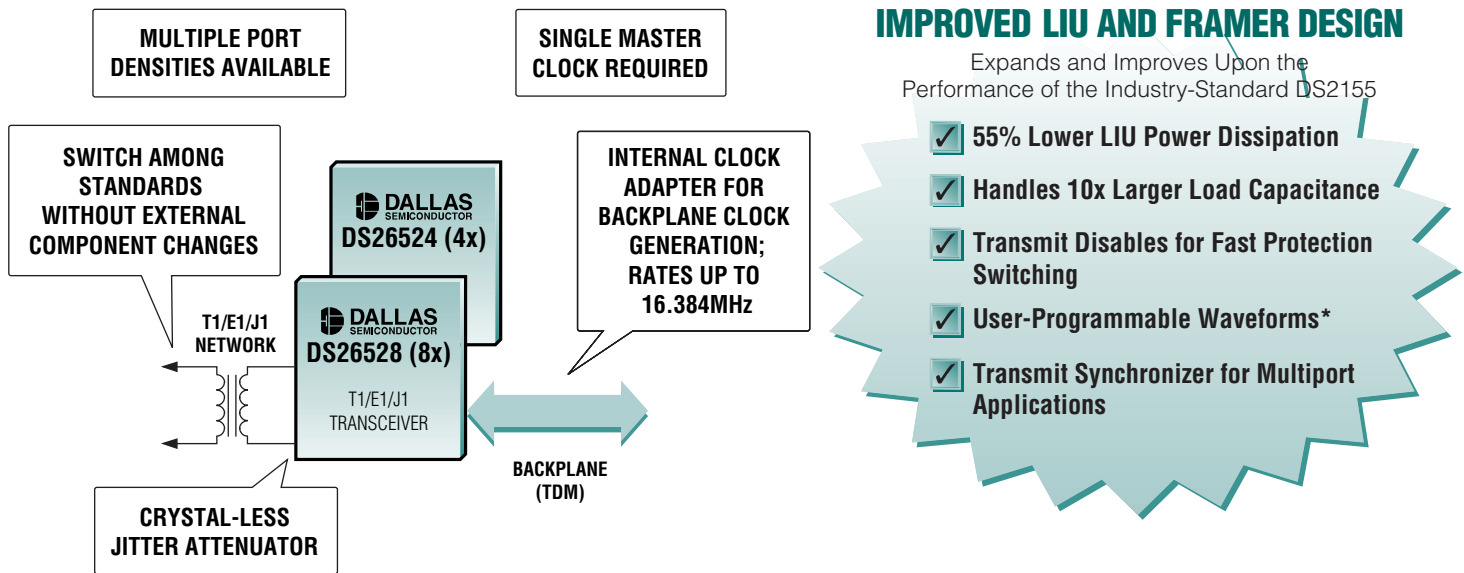


# NEXT-GENERATION MULTIPOINT, T1/E1/J1, SINGLE-CHIP TRANSCEIVERS

## Software-Compatible Family of LIU + Framer Combinations Switch Among T1, E1, and J1 Standards Without External Component Changes

The TEX series of transceivers are software-compatible, multipoint, single-chip T1/E1/J1 devices that provide compact solutions for high-port-count applications. With 8- and 4-port versions, the TEX family contains all the functions necessary to connect to T1, E1, or J1 lines without relays or switched external components. The TEX transceivers have a common feature set and use a common architecture and register map, all of which allow easy migration among port densities and the reuse of existing software.



- ◆ Independent Software-Selectable Port Configurations
- ◆ LH/SH LIU with -43dB Sensitivity
- ◆ Internal Software-Selectable Line Termination
- ◆ Flexible Signaling Extraction and Insertion
- ◆ Two-Frame Elastic Store Buffers for Each Transmitter and Receiver
- ◆ Full-Featured BERT for Each Transceiver

Part	No. of Ports	Temp Range (°C)	Pin-Package
DS26528	8	-40 to +85	256-CSBGA (17mm x 17mm)
DS26524	4	-40 to +85	256-CSBGA (17mm x 17mm)

\* Please contact the factory for detailed information on user-programmable waveforms.



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