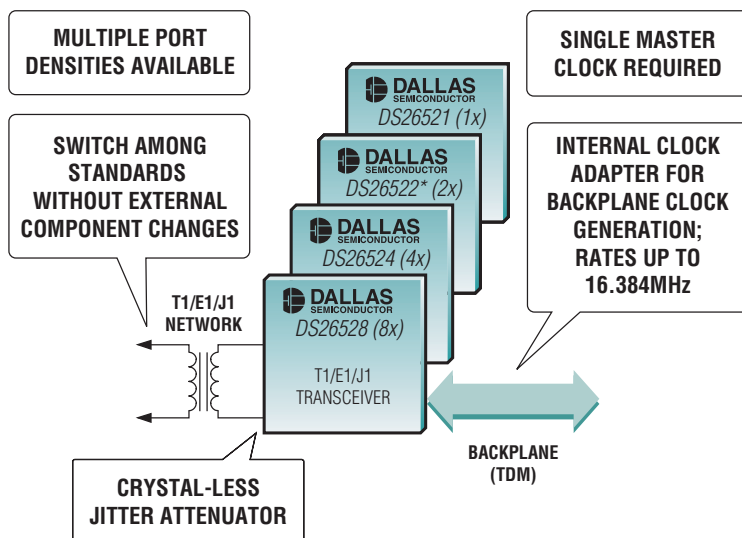


NEXT-GENERATION MULTIPOINT, T1/E1/J1, SINGLE-CHIP TRANSCEIVERS

Software-Compatible Family of LIU + Framer Combinations Switch Among T1, E1, and J1 Standards Without External Component Changes

The TEX series of transceivers are software-compatible, multipoint, single-chip T1/E1/J1 devices that provide compact solutions for high-port-count applications. With 8-, 4-, 2- and 1-port versions, the TEX family contains all the functions necessary to connect to T1, E1, or J1 lines without relays or switched external components. The TEX transceivers have a common feature set and use a common architecture and register map, all of which allow easy migration among port densities and the reuse of existing software.



IMPROVED LIU AND FRAMER DESIGN

Expands and Improves Upon the Performance of the Industry-Standard DS2155

- ✓ 30% Lower LIU Power Dissipation Per Port
- ✓ Handles 10x Larger Load Capacitance
- ✓ Transmit Disables for Fast Protection Switching
- ✓ User-Programmable Waveforms**
- ✓ Transmit Synchronizer for Multipoint Applications

- ◆ Independent Software-Selectable Port Configurations
- ◆ LH/SH LIU with -43dB Sensitivity
- ◆ Internal Software-Selectable Line Termination
- ◆ Flexible Signaling Extraction and Insertion
- ◆ Two-Frame Elastic Store Buffers for Each Transmitter and Receiver
- ◆ Full-Featured BERT for Each Transceiver

*Future product—contact factory for availability.

**Please contact the factory for detailed information on user-programmable waveforms.

Part	No. of Ports	Temp Range (°C)	Package (mm x mm)
DS26521L	1	-40 to +85	64-LQFP (10 x 10)
DS26522G*	2	-40 to +85	144-CSBGA (13 x 13)
DS26524G	4	-40 to +85	256-CSBGA (17 x 17)
DS26528G	8	-40 to +85	256-CSBGA (17 x 17)



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